

Diagram of the UR67-EC-78-16DIO-12-P chassis showing 8 modules and their connections. The modules are numbered 1 through 8. The connections are labeled as follows:

- 1: IN, OUT
- 2: A, B
- 3: A, B
- 4: A, B
- 5: A, B
- 6: A, B
- 7: A, B
- 8: A, B

Legend:

- LINKACT: Link Actuator
- RUN: Run
- ERR: Error
- U: Up
- D: Down

接线示意图		接口定义说明	
PNP输入 (2线制)			
PNP输入 (3线制)			M12x8 I/O Pin1 +24V Pin2 DI_B/DO_B Pin3 GND 0V Pin4 DI_A/DO_A Pin5 FE
PNP输出			
供电		7/8" POWER Pin1 GND U_L Pin2 GND U_S Pin3 FE(PE) Pin4 +24V U_S Pin5 +24V U_L M12x2 NET Pin1 TD+ Pin2 RD+ Pin3 TD- Pin4 RD- BUS	

General Tolerances:		Tolerances ISO 9015	
Changes: PLM		PLM	
Mat. No. (SAP) 3076260000		Drawing no. _____	
PLM		Scale: ... Sheet 1 / 1	
Drawn	PLM		
Responsible	PLM		
Approved	PLM		